



**NOTE:** All signals other than power/ground and those marked with '\*' can be used as general purpose I/O (GPIO).

Pin	Processor Signal	Function(s)
JP1 - 1	DDP	UDP device*
2	DDM	UDP device*
3	GND	
JP2 - 1	+3.3VDC	
2	GND	
3	PB14	DRXD
4	PB15	DTXD
JP3 - 1	+3.3VDC	
2	GND	
JP4 - 1	+3.3VD	
2	GND	
SV1 - 1	PB31	PCK1/ISI_MCK
2	PB30	PCK0/ISI_HSYNC
3	GND	
4	GND	
5	PB29	CTS1/ISI_VSYNC
6	PB28	RTS1/ISI_PCK
7	GND	
8	GND	
9	PB1	SPI1_MOSI/TIOB3
10	PA31	SCK0/TXD4
11	PB19	RD0/TIOB5
12	PB4	TXD0
13	VDDIOP1	ISI_VDD*
14	+3.3VDC	
15	PB13	RXD5/ISI_D11
16	PB12	TXD5/ISI_D10
17	PB11	RXD3/ISI_D9
18	PB10	TXD3/ISI_D8
19	PB27	CTS0/ISI_D7
20	PB26	RTS0/ISI_D6
21	PB25	RI0/ISI_D5
22	PB24	DTR0/ISI_D4
23	PB23	DCD0/ISI_D3
24	PB22	DSR0/ISI_D2

SV1 - 25	PB21	RF0/ISI_D1
26	PB20	RK0/ISI_D0
SV2 - 1	+3.3VDC	
2	GND	
3	PC3	SPI1_NPCS3/AD3
4	PC1	PCK0/AD1
5	PC2	PCK1/AD2
6	PC0	SCK3/AD0
SV3 - 1	+3.3VDC	
2	PC14	NCS3/NANDCS/IRQ2
3	PC6	TIOB2/CFCE1
4	PC5	A24/SPI1_NPCS1
5	PC8	NCS4/CFCS0/RTS3
6	PC11	NCS2/SPI0_NPCS1
7	PC4	A23/SPI1_NPCS2
8	PC13	FIQ/NCS6
9	GND	
10	GND	
11	PC9	NCS5/CFCS1/TIOB0
12	PC10	A25/CFRNW/CTS3
13	PC7	TIOB1/CFCE2
14	PC12	IRQ0/NCS7